

INSTITUTE OF Recognised by UGC under section 2(f) &12(B)
Approved by AICTE - New Delhi
Permanently Affiliated to JNTUK, SBTET,
Ranked as "A" Grade by Govt. of A.P.,

College Code K6

## **Examination Section**

Academic year 2019-2020

## II B.Tech II Semester (SITE18) Practical End Semester Examinations, Oct-2020 Schedule

Timings: Batch 1 ( $B_1$ ): 9.00 a.m – 12.00 p.m

Batch 2 (B<sub>2</sub>): 11.00 a.m - 02.00 p.m

Batch 3 (B<sub>3</sub>): 01.00 p.m – 04.00 p.m

Batch 4 ( $B_4$ ): 02.00 p.m – 05.00 p.m

S. No.	Branch	Name of the Laboratory	Student strength	03/11/2020				04/11/2020				05/11/2020				06/11/2020			
				B <sub>1</sub>	$\mathbf{B}_2$	$\mathbf{B}_3$	B4	<b>B</b> <sub>1</sub>	B <sub>2</sub>	<b>B</b> <sub>3</sub>	<b>B4</b>	B <sub>1</sub>	B <sub>2</sub>	$\mathbf{B}_3$	<b>B4</b>	B <sub>1</sub>	$\mathbf{B}_2$	$\mathbf{B}_3$	B4
1	CE	Strength of Materials	64	$C_1$	C <sub>2</sub>	C <sub>3</sub>	C <sub>4</sub>							12					
		Environmental Engineering						$C_1$	C <sub>2</sub>	C <sub>3</sub>	C <sub>4</sub>								
		Materials Testing Geology										$C_1$	$C_2$	$C_3$	C <sub>4</sub>				
2	EEE	Digital Electronics	108	EE <sub>1</sub>	EE <sub>2</sub>			EE <sub>3</sub>	EE <sub>4</sub>			EE <sub>5</sub>	EE <sub>6</sub>	EE <sub>7</sub>					
		Control Systems		EE <sub>5</sub>	EE <sub>6</sub>	EE <sub>7</sub>		EE <sub>1</sub>	EE <sub>2</sub>			EE <sub>3</sub>	EE <sub>4</sub>						
		Electrical Machines - II		EE <sub>3</sub>	EE <sub>4</sub>			EE5	EE <sub>6</sub>	EE <sub>7</sub>		EE <sub>1</sub>	EE <sub>2</sub>						
3	ME	Fluid Mechanics & Fluid Machines	188	$M_1$	M <sub>2</sub>	$M_3$		$M_4$	$M_5$	M <sub>6</sub>		M <sub>7</sub>	M <sub>8</sub>	M <sub>9</sub>		M <sub>10</sub>	M <sub>11</sub>	M <sub>12</sub>	M <sub>13</sub>
		Mechanics of Solids & Materials		$M_{10}$	$M_{11}$	$M_{12}$	$M_{13}$	$M_1$	$M_2$	$M_3$		$M_4$	$M_5$	$M_6$		$M_7$	M <sub>8</sub>	M <sub>9</sub>	
	ECE	Digital System Design	197	$EC_1$	EC <sub>2</sub>	EC <sub>3</sub>		EC <sub>4</sub>	EC <sub>5</sub>	EC <sub>6</sub>		EC <sub>7</sub>	EC <sub>8</sub>	EC <sub>9</sub>		EC <sub>10</sub>	$EC_{11}$	EC <sub>12</sub>	EC <sub>13</sub>
4		Analog Circuits		EC <sub>7</sub>	EC <sub>8</sub>	EC <sub>9</sub>		EC <sub>10</sub>	EC <sub>11</sub>	EC <sub>12</sub>	EC <sub>13</sub>	EC <sub>1</sub>	EC <sub>2</sub>	EC <sub>3</sub>		EC <sub>4</sub>	EC <sub>5</sub>	EC <sub>6</sub>	
		Analog & Digital Communications		EC <sub>4</sub>	EC <sub>5</sub>	EC <sub>6</sub>		EC <sub>1</sub>	EC <sub>2</sub>	EC <sub>3</sub>		EC <sub>10</sub>	EC <sub>11</sub>	EC <sub>12</sub>	EC <sub>13</sub>	EC <sub>7</sub>	EC <sub>8</sub>	EC <sub>9</sub>	
5	CSE	Computer Organization	184	CS <sub>1</sub>	CS <sub>2</sub>	CS <sub>3</sub>		CS <sub>4</sub>	CS <sub>5</sub>	CS <sub>6</sub>		CS <sub>7</sub>	CS <sub>8</sub>	CS <sub>9</sub>		CS <sub>10</sub>	CS <sub>11</sub>	CS <sub>12</sub>	
		Algorithm Design and Analysis		CS <sub>7</sub>	CS <sub>8</sub>	CS <sub>9</sub>		CS <sub>1</sub>	CS <sub>2</sub>	CS <sub>3</sub>		CS <sub>10</sub>	CS <sub>11</sub>	CS <sub>12</sub>		CS <sub>4</sub>	CS <sub>5</sub>	CS <sub>6</sub>	
		Java Programming		CS <sub>4</sub>	CS <sub>5</sub>	CS <sub>6</sub>		$CS_{10}$	CS <sub>11</sub>	CS <sub>12</sub>		$CS_1$	CS <sub>2</sub>	CS <sub>3</sub>		CS <sub>7</sub>	CS <sub>8</sub>	CS <sub>9</sub>	
	IT	Computer Organization	49									$I_1$	$I_2$	$I_3$				-0	
6		Algorithm Design and Analysis		$I_1$	I <sub>2</sub>	$I_3$													
		Java Programming						$I_1$	$I_2$	$I_3$									

Controller of Examinations

Principal