



sasi
INSTITUTE OF
TECHNOLOGY &
ENGINEERING
autonomous

Accredited by NAAC with "A" Grade.
Recognised by UGC under section 2(f) & 2(B)
Approved by AICTE - New Delhi
Permanently Affiliated to JNTUK, SBTET,
Ranked as "A" Grade by Govt. of A.P.,

College
Code
K6

Examination Section

Academic year 2018-19

I M.Tech II Semester (SITE18) Practical End Examinations, May-2019 Schedule

Timings: 9.00 a.m – 12.00 p.m

Sl. No.	Branch	Specialization	Name of the Lab	Student strength	Date
1	ECE	VLSI & Embedded Systems	Analog VLSI Design Lab (18ECVE251)	4	06-05-2019
2	EEE	Power Electronics	Electrical Drives Laboratory (18EEPE251)	1	


Controller of Examinations


Principal