



sasi INSTITUTE OF
TECHNOLOGY &
autonomous ENGINEERING

Accredited by **NAAC** with "A" Grade,
Recognised by **UGC** under section 2(f) & 12(B)
Approved by **AICTE** - New Delhi
Permanently Affiliated to **JNTUK, SBTET**,
Ranked as "A" Grade by Govt. of A.P.,

College
Code
K6

Examination Section

Academic Year 2018-19

M.Tech I Semester Autonomous (SITE18 Regulations) **I MID** Examinations

Time-Table

MID Timings: 10.00A.M To 12.00 P.M.

| Branch \ Date | 08.10.2018 (Monday) | 09.10.2018 (Tuesday) | 10.10.2018 (Wednesday) | 11.10.2018 (Thursday) | 12.10.2018 (Friday) | 13.10.2018 (Saturday) |
|--|--|---|--|---|--|---|
| EEE (Power Electronics) | Modelling & Analysis of Electrical Machines (18EEPE101) | Analysis of Power Electronic Converters (18EEPE102) | Power Semi Conductor Devices Modeling (18EEPE103) | Power Electronic Control of DC Drives (18EEPE104) | HVDC Transmission (18EEPE105) | Static VAR Compensators & Harmonic Filtering (18EEPE106) |
| ECE (VLSI & ES) | CMOS Analog IC Design (18ECVE102) | Microcontrollers for Embedded System Design (18ECVE106B) | Principles of Embedded Systems (18ECVE105) | Programming Concepts using Verilog HDL (18ECVE101) | Advanced Digital Design (18ECVE104C) | IC Fabrication Technology (18ECVE103) |

Controller of Examinations

Date: 05th October, 2018

Copy to HOD, EEE - ECE

Principal