

About the Institution:



Sasi Institute of Technology & Engineering (SITE) under the aegis of Sasi Educational Society was started in the year 2002. Sasi Educational Society has been imparting quality education for over 34 years. SITE is approved by AICTE, New Delhi and accredited by NAAC with 'A' grade in the year 2014 and permanently affiliated to JNTUK Kakinada in the year 2015. SITE is providing best education in engineering and technology over two decades; it has made its mark already in the minds of millions. It is treated as the best in coastal belt of Andhra Pradesh for professional education. SITE stands with highest success rate not only in academic lines but also in placements.

About the Department:

Department of Electrical and Electronics Engineering was started in 2002. The present intake is 120 students for the Program B.Tech. in EEE and 24 students in M.Tech. Power Electronics. The department functions with a vision to expand as a centre of excellence in the field of Electrical Engineering through industrial and academic research by training the learners for global acceptance. The department has well equipped laboratories, qualified and experienced faculty. The department was accredited by NBA in the academic year 2014-15.

About the Program:

The main aim and motivation for organizing this faculty development program is to provide an exposure to the academicians, research scholars and industry personnel on recent trends in Power electronic converters and their switching strategies by providing a platform for them to learn and interact on the current developments in this area.

The specific objectives are to

- Expose the participants to innovative and challenging research areas
- Familiarize the current trends in Power electronic converters and their switching strategies
- To provide the fundamental knowledge and explore the applications of AI techniques for power electronic converter control in the area of Power Systems

Contents:

- Simulation and design of power electronic converters
- AI techniques for power electronic applications
- Power electronic converters for grid interface of solar power generation
- Soft Computing Techniques in Wind Energy Systems
- Resonant converters: Design and applications
- Custom power devices in power distribution system
- Power electronics in power quality
- Multi- level inverters and their PWM switching schemes
- Power electronic interface in automotive applications, renewable energy system and grid integration
- Predictive current control of multi level inverters

Resource Persons:

Dr. Srirama Srinivas, Asso. Prof., IIT Madras
Dr. Srinivas Bhaskar K, Asst. Prof., IIT Bhubaneswar

Dr. K.Siva Kumar, Asst. Prof., IIT Hyderabad

Dr. N.Viswanathan, Prof., NIT Warangal

Dr. L. Ashok Kumar, Prof.,

PSG college of Tech, Coimbatore

Dr. S. Albert Alexander, Asst. Prof.,

Kongu Enng College, Tamilnadu

Mr. Aswani Kumar Eedara,

Head of the EEE Department, SITE

Chief Patrons:

Sri B. Venu Gopala Krishna, Chairman, SITE

Sri B. Ravi Kumar, Vice Chairman, SITE

Patron:

Dr. K. Bhanu Prasad, Principal, SITE

Convenor and Coordinator:

Mr. Aswani Kumar Eedara

Head of EEE Department, SITE

Co-Coordinator:

Mr. M. Krishna Chaitanya

Asst. Prof., Department of EEE

Organizing Committee:

Mr. G.Satyanarayana Rao	Mr. A.Srinivas
Mr. V.L.N.Sastry	Mr. M.R.V. Murali
Mr. M.P.Prathap	Mr. B. Raghavendra
Mr. P.Hema Sundar	Mr. K.N.V. Siva
Mr. K.Srinivasa Rao	Mr. K.Nanajee
Mr. M. N.Murthy	Mr. N.Nagendra
Mr. L.V.Narasimha	Mr. G.Satyanarayana
Ms. K. Phani Santoshi	Mr. Ch.Kumar raja
Mr. M.V. Raghavendra	Mr. V.B.Sundheep

Registration Fee:

Academicians : Rs. 1500/-
PG Students : Rs. 1000/-
Industry delegates : Rs. 2000/-

Registration charge includes certificate of participation and working lunch. Registration is for limited no. of participants and is based on first come first served. Interested participants are requested to fill the registration form and send it along with demand draft to the coordinator on or before 7th January 2016. **Registration fee will be accepted by Demand Draft drawn in favour of "H.O.D, ELECTRICAL AND ELECTRONICS ENGINEERING", payable at Tadepalligudem.** (Any Nationalized Bank)

Important Dates:

Last date of receipt of registration form:

07-01-2016

Intimation of selection: **09-01-2016**

For accommodation contact

Mr. N. Nagendra, Ph. 9010331693

Address for Communication:

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hodeee@sasi.ac.in
chaitanya.eee@sasi.ac.in

Registration Form

Name:

Designation:

Organization:

Address:

PIN Code:

Phone:

Email:

DD Particulars:

Amount Rs.: Date:

DD No.: Bank:

Date: Signature of the applicant

Signature of the Sponsoring Authority

*A Three Day Faculty
Development Program on*

“Advanced Power Electronics: Applications and Control”

21st - 23rd January 2016

Organized by
**Department of Electrical and
Electronics Engineering**



Sasi Institute of Technology & Engineering

(Accredited by NBA, NAAC with A grade, Approved
by AICTE, New Delhi, Permanently Affiliated to
JNTUK, Kakinada & SBTET Hyderabad)
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